

078

Notice of Allowability

Application No.

09/801,900

Examiner

Jeanne A. Di Grazio

Applicant(s)

TAKEISHI ET AL.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to RCE 15 September 2004.
2. ☒ The allowed claim(s) is/are 1-54.
3. ☒ The drawings filed on 09 March 2001 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).**
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 25 April 2003
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

DETAILED ACTION

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in both a personal interview and confirmed by telephone interview with Mr. Roland Long on November 18, 2004 and November 26, 2004, respectively.

The application has been amended as follows:

As to claim 1, please insert -- a signal processing circuit substrate configured to drive a liquid crystal display unit -- after "a signal processing circuit substrate" in line 4.

As to claim 20, please insert -- a signal processing circuit substrate configured to drive a liquid crystal display unit -- after "a signal processing circuit substrate" in line 4.

As to claim 40, please insert -- a signal processing circuit substrate configured to drive a liquid crystal display unit -- after "a signal processing circuit substrate" in line 1 (top of page 12).

As to claim 47, please insert -- a signal processing circuit substrate configured to drive a liquid crystal display unit -- after "a signal processing circuit substrate" in line 13 (of page 14).

The following is an examiner's statement of reasons for allowance:

As to claim 1, relevant art of record did not disclose, alone or in combination, a signal processing circuit substrate assembly configured to drive a liquid crystal display unit comprising a device electrically and mechanically mounted on a first surface of a mounting member, the device having a variable value and including a value adjustment portion through which the variable value is adjusted and wherein the value adjustment portion faces a through-hole and in combination with Applicant's other recited limitations.

The above limitations result in a novel variable device in a signal processing circuit substrate assembly configured to drive a liquid crystal display unit.

Relevant art United States Patent 4,646,158 (to Ohno et al.), United States Patent 6,409,159 B1 (to Asai et al.) and United States Patent 5,703,665 (to Muramatsu et al.) teach and disclose a liquid crystal television receiver having a variable device, method of supporting a printed circuit board and a method of mounting electric components, and resilient support members arranged at vertices of an isosceles triangle, respectively. However, the prior art of record does not teach or fairly suggest Applicant's recited elements and furthermore in the context of a signal processing circuit substrate configured to drive a liquid crystal display unit.

As to claim 20, relevant art of record did not disclose, alone or in combination, a signal processing circuit substrate assembly configured to drive a liquid crystal display unit comprising a device mounted on a first surface of a signal processing circuit substrate, the device having a variable value and including a value adjustment portion through which the variable value is adjusted and wherein the value adjustment portion faces a through-hole and the device is in a displaceable floating condition and in combination with Applicant's other recited limitations.

The above limitations result in a novel variable device in a signal processing circuit substrate assembly configured to drive a liquid crystal display unit.

Relevant art United States Patent 4,646,158 (to Ohno et al.), United States Patent 6,409,159 B1 (to Asai et al.) and United States Patent 5,703,665 (to Muramatsu et al.) teach and disclose a liquid crystal television receiver having a variable device, method of supporting a printed circuit board and a method of mounting electric components, and resilient support members arranged at vertices of an isosceles triangle, respectively. However, the prior art of record does not teach or fairly suggest Applicant's recited elements and furthermore in the context of a signal processing circuit substrate configured to drive a liquid crystal display unit.

As to claim 40, relevant art of record did not disclose, alone or in combination, a method of fabricating a signal processing circuit substrate assembly configured to drive a liquid crystal display unit comprising the steps of mounting a device on a first surface of a flexible member and signal processing circuit substrate, the device having a variable value and including a value adjustment portion through which the variable value is adjusted and wherein the value adjustment portion faces a through-hole and in combination with Applicant's other recited limitations.

The above limitations result in a novel method of fabricating a signal processing circuit substrate assembly configured to drive a liquid crystal display unit.

Relevant art United States Patent 4,646,158 (to Ohno et al.), United States Patent 6,409,159 B1 (to Asai et al.) and United States Patent 5,703,665 (to Muramatsu et al.) teach and disclose a liquid crystal television receiver having a variable device, method of supporting a printed circuit board and a method of mounting electric components, and resilient support

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members arranged at vertices of an isosceles triangle, respectively. However, the prior art of record does not teach or fairly suggest Applicant's recited elements and furthermore in the context of a signal processing circuit substrate configured to drive a liquid crystal display unit.

As to claim 47, relevant art of record did not disclose, alone or in combination, a method of fabricating a signal processing circuit substrate assembly configured to drive a liquid crystal display unit comprising the steps of patterning a flexible printed circuit sheet into patterns which make flexible printed circuits, fixing a flexible printed circuit sheet onto a first surface of a signal processing circuit substrate such that a value adjustment portion of a device is in alignment with a through-hole formed throughout the signal processing circuit substrate.

The above limitations result in a novel method of fabricating a signal processing circuit substrate assembly configured to drive a liquid crystal display unit.

Relevant art United States Patent 4,646,158 (to Ohno et al.), United States Patent 6,409,159 B1 (to Asai et al.) and United States Patent 5,703,665 (to Muramatsu et al.) teach and disclose a liquid crystal television receiver having a variable device, method of supporting a printed circuit board and a method of mounting electric components, and resilient support members arranged at vertices of an isosceles triangle, respectively. However, the prior art of record does not teach or fairly suggest Applicant's recited elements and furthermore in the context of a signal processing circuit substrate configured to drive a liquid crystal display unit.

As to claims 2-19, 21-39, 41-46 and 48-54, they are dependent either directly or indirectly upon claims with allowable subject matter above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeanne A. Di Grazio whose telephone number is (571)272-2289. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim, can be reached on (571)272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jeanne Andrea Di Grazio
Patent Examiner
Art Unit 2871

JDG


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JAN 11 2010
EXAMINER
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